Lockable Obfuscation

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Abstract—In this paper we introduce the notion of lockable obfuscation. In a lockable obfuscation scheme there exists an obfuscation algorithm Obf that takes as input a security parameter, a program P, a message msg and "lock value" lck and outputs an obfuscated program oP. One can evaluate the obfuscated program oP on any input x where the output of evaluation is the message msg if P(x) = lck and otherwise receives a rejecting symbol.

We proceed to provide a construction of lockable obfuscation and prove it secure under the Learning with Errors (LWE) assumption. Notably, our proof only requires LWE with polynomial hardness and does not require complexity leveraging.

We follow this by describing multiple applications of lockable obfuscation. First, we show how to transform any attribute-based encryption (ABE) scheme into one in which the attributes used to encrypt the message are hidden from any user that is not authorized to decrypt the message. (Such a system is also know as predicate encryption with one-sided security.) The only previous construction due to Gorbunov, Vaikuntanathan and Wee is based off of a specific ABE scheme of Boneh et al. By enabling the transformation of any ABE scheme we can inherent different forms and features of the underlying scheme such as: multi-authority, adaptive security from polynomial hardness, regular language policies, etc.

We also show applications of lockable obfuscation to separation and uninstantiability results. We first show how to create new separation results in circular encryption that were previously based on indistinguishability obfuscation. This results in new separation results from learning with error including a public key bit encryption scheme that it IND-CPA secure and not circular secure. The tool of lockable obfuscation allows these constructions to be almost immediately realized by translation from previous indistinguishability obfuscation based constructions.

In a similar vein we provide random oracle uninstantiability results of the Fujisaki-Okamoto transformation (and related transformations) from the lockable obfuscation combined with fully homomorphic encryption. Again, we take advantage that previous work used indistinguishability obfuscation that obfuscated programs in a form that could easily be translated to lockable obfuscation.

I. Introduction

The topic of indistinguishability obfuscation has received an tremendous amount of attention from the cryptographic community over the last several years. Initially, the concept was introduced by Barak et al. [1], [2] as an possible alternative to the notion of virtual black box obfuscation which they showed to be impossible to achieve for some functionalities. However, the concept indistinguishability obfuscation did not receive much immediate attention since (1) there were no such obfuscation candidates at the time and (2) the perceived lack of applications due to the fact that it only guaranteed security between two functionally equivalent circuits.

In 2013, two works in the literature addressed these questions. First, Garg et al. [3] provided the first indistinguishability obfuscation candidate using the Garg, Gentry and Halevi [4] multilinear map candidate. Then Sahai and Waters [5] introduced the "punctured programming" methodology for building cryptographic primitives from indistinguishability obfuscation which was used in their work and several subsequent works to resolve many open problems in cryptography.

When the potential of indistinguishability obfuscation was exposed, attention naturally moved to establishing security of obfuscation candidates since the original work of Garg et al. [3] only provided a heuristic argument of security. Initial work in this line attempted to prove security under certain multilinear map models or assumptions [6], [7], [8], [9], [10]. However, the security guarantees delivered from such proofs could only be as strong as the underlying multilinear map candidates [4], [11], [12], [13] which have been under a steady stream of cryptanalysis (see e.g. [14], [15], [16], [17], [18], [19], [20], [21], [22], [23], [24], [25], [26] and the references therein). To combat this there have been new multilinear map candidates proposed as well as models meant to capture most existing attacks [27], [28]. While these techniques present progress in defense against currently known cryptanalysis, it is unclear whether they can be connected to standard assumptions. Another set of works [29], [30], [31], [32], [33], [34], [35] have shown connections between certain types of functional encryption schemes and indistinguishability obfuscation with results showing that a constant degree multilnear maps combined with a constant depth PRG give indistinguishability obfuscation.

In this paper we approach the problem of achieving provably secure obfuscation from a different direction. Our philosophy is to anchor ourselves to the Learning with Errors (LWE) assumption and explore what applications and forms



of obfuscation are achievable. Here we propose and define a new form of obfuscation that we call lockable obfuscation for which we give a construction that is provably secure under the LWE assumption. In addition, we show several applications of lockable obfuscation that were only known to this point under indistinguishability obfuscation.

We begin by informally introducing the notion of lockable obfuscation. A lockable obfuscation scheme consists of an obfuscation and evalution algorithms. The (randomized) obfuscation algorithm Obf takes as input a security parameter λ , a program P, a message msg and lock value α and outputs an obfuscated program \widetilde{P} . The evaluation algorithm Eval takes as input an obfuscated program \widetilde{P} and an input x. If $P(x) = \alpha$ then the evaluation algorithm outputs the message msg, where P, msg and α were the program, message and lock values input to create \widetilde{P} . Otherwise, if $P(x) \neq \alpha$ the evaluation algorithm (with high probability) outputs the \bot symbol to indicate rejection.

Intuitively, security states that if the lock value is chosen at random and kept from the attacker, then the attacker cannot learn anything about the program and message other than their sizes. That is there exist a simulator Sim such that for all program message pairs P, msg

$$\begin{split} \{ \mathsf{Obf}(1^\lambda, P, \mathsf{msg}, \alpha) : \alpha \leftarrow \{0, 1\}^{m(\lambda)} \} \\ \approx_c \\ \mathsf{Sim}(1^\lambda, 1^{|P|}, 1^{|\mathsf{msg}|}). \end{split}$$

We show how to construct a lockable obfuscation scheme for any polynomial sized circuit of sufficient output length from the Learning with Errors assumption. Our construction relies only on the polynomial hardness of the assumption (i.e. unlike witness encryption/indistinguishability obfuscation constructions, there is no sub-exponential hardness or complexity leveraging involved). ¹ For this reason lockable obfuscation could be a preferred abstraction for building certain primitives even in a possible future where indistinguishability obfuscation is realizable from the assumption of LWE with subexponential hardness. The reason we don't require subexponential hardness of LWE is that the security of our construction is derived from the hidden lock value α . In particular, our proof of security does not step through each possible input like many reductions to witness encryption or indistinguishability obfuscation [9], [10], [29], [30], [31]. Also, note that since the lock value is chosen at random (and independent of the program), therefore lockable obfuscation could also be interpreted as obfuscation for a family of evasive functions [36].

We will defer the explanation of our construction and proof to the technical overview of Section I-A and move on to discussing applications.

Hiding Attributes in Attribute-Based Encryption: In a (key-policy) Attribute-Based Encryption (ABE) system [37] a user will encrypt a message msg under an attribute string x. A private key, as issued by an authority, will be associated with a boolean function f. When a user holding a secret key for function f attempts to decrypt a ciphertext he will learn the message if and only if f(x) = 1. Otherwise, the message remains hidden. While an ABE ciphertext will securely hide a private message, its security definition provides no guarantees about hiding the attribute string x. This can be problematic in many practical scenarios. Suppose we use ABE to encrypt email messages and use header metadata such as the sender, recipients, time and subject as attributes, where access functions are written over these attributes. In many settings this metadata itself will be very sensitive and disclosing it as part of the ciphertext is undesirable.

Almost all expressive² ABE systems built from standard tools³ allow for an attacker to learn the attribute string associated with a ciphertext. One notable exception is the work of Gorbunov, Vaikuntanathan and Wee [40] that achieves one-sided security in hiding attributes. In that their construction hides the attribute string x as long as f(x) = 0 for all functions f that the adversary has a secret key for. This gives a much improved security picture to before as in our example an attacker will not be able to learn the header metadata of the emails so long as they are not authorized to decrypt them. We will call such a scheme a predicate encryption scheme with one-sided security.

The GVW construction was built by adapting the ABE system of Boneh et al. [41] and required an intricate knowledge of the original system in order to utilize certain special mathematical properties. The resulting construction achieves the same functionality of bounded depth circuits as the original as well as maintains selective security under the Learning with Errors assumption.

In this paper we show how to use lockable obfuscations (sometimes in combination with fully homomorphic encryption) to generically transform *any* ABE scheme into a predicate encryption scheme with one-sided security with corresponding functionality. An advantage of using a generic transformation is that it can take advantages of the features or forms of ABE constructions that have been introduced over the last 10+ years. including key-policy [42], ciphertext-policy [43], adaptive security (without complexity leveraging) [44], multi-authority [45], [46], [47], and efficient expression of keys as deterministic finite automata [48], [49] and circuits [50], [41]. Currently, there is no single ABE construction from standard tools that simultaneously

¹Note that we still require subexponential LWE modulus. This translates to a subexponential approximation factor in the worst case hardness of lattice problems.

²We note that there are constructions of more limited expressivity such as vector matching [38] or inner product testing [39] that achieve such security.

³For the purposes of this discussion we roughly consider number theoretic constructions grounded on RSA, bilinear maps and (Ring) LWE to be standard tools and those based on multilinear maps or indistinguishability obfuscation not to be.

delivers all such features. The most desirable ABE form can vary significantly between applications. Our transformations will allow one to inherent the properties of the underlying ABE scheme and at the same obtain one-sided hiding of the ciphertext attributes.

The technique for hiding attributes is fairly simple. To perform a (key-policy) encryption to attributes x and message msg the encryption algorithm first chooses a random lock value α . Next it creates a (sub) ciphertext C which is an encryption of the message α under the attribute string x in the original ABE system. Now consider the program P which takes as input a secret key and then decrypts C and outputs the result. The final ciphertext ct is a lockable obfuscation of this program P, under the lock value α and message msg. Key generation is the same as in the original scheme and decryption is simply using the lockable obfuscation evaluation algorithm on the ct with the secret key as the input.

Correctness can be observed. Suppose a user has a secret key sk_f for function f and applies it to a ciphertext encryption with attributes x where f(x)=1). The evaluation algorithm will output msg since $P(\operatorname{sk}_f)=\alpha$, which is the lock value. On the other hand suppose that the attacker does not have any secret keys for a function f where f(x)=1, then by the message hiding security of the underling ABE scheme, he won't be able to distinguish an encryption of the lock value α from an encryption of the all 0's string. Now that the lock value is hidden, one can take an additional hybrid step to argue that this is indistinguishable from a simulated obfuscation to erase knowledge of the program P including C and the attribute string x.

The above construction works for any scheme with an apriori bounded size decryption circuit. By adding a level of indirection one can leverage leveled homomorphic encryption (which is realizable under LWE [51], [52]) to upgrade this to any scheme with an apriori decryption circuit depth. Or leverage fully homomorphic encryption to work without any depth bounds. The details of the transformation are given in the full version.

Separation and Uninstantiability Results: We now demonstrate the power of lockable obfuscation for achieving negative results in cryptography by focusing on two families of separation and uninstantiability results.

In recent years there has been a significant interest on the problem of circular security [53], [54], [55] perhaps in large part due to Gentry's [56] result showing that a leveled homomorphic encryption scheme that is circular secure implies unbounded fully homomorphic encryption. Roughly, an encryption system is circular secure if it maintains security in the setting where there are n pairs of public keys and ciphertexts arranged such that the i^{th} ciphertext encrypts the $(i+1)^{th}$ secret key.

There have been several separation results [57], [58], [59], [60], [61], [62], [63], [64], [65], [66] showing that

such security does not come for free. In particular, they show in different contexts that there exist schemes that are IND-CPA secure, but not circular secure. (We discuss this prior work in detail in the full version). A natural dichotomy of these results is between separations achieved using indistinguishability obfuscation [60], [65] and those built from standard assumptions [57], [58], [62], [63], [64], [66] such as assumptions on bilinear groups or LWE.

Separations built on indistinguishability obfuscation have the advantage that they can be developed relatively rapidly and are also relatively simple as one can build constructions using "normal" programs without diving into number theory. The disadvantage is that indistinguishability is not currently known from any standard assumptions. On the flip side, the number theoretic constructions are based on much more standard assumptions. However, they developing and understanding such solutions is a much more arduous task.

In the full version, we show how to translate existing two indistinguishability obfuscation separation results due to Koppula, Ramchen and Waters [60] and one result due Goyal, Koppula and Waters [65] to rely on lockable obfuscation. The translations are extremely straightforward and our resulting solutions are almost identical with the exception that we use lockable obfuscation. The main insight is that the programs obfuscated in the above results come in a lockable friendly form. In particular, they perform a sequence of computations on the input that result in a value s. Then the program tests and reports if PRG(s) = t for a pseudorandom generator PRG and hardwired value t. Using lockable obfuscation one simply uses s as the output (which is a possible lock value).

Our translations of these prior works to lockable obfuscation lead to separations that can be built on the Learning with Errors assumption. Concretely, we obtain new results from LWE that were previously known only from indistinguishability obfuscation. (1) We show how to build a *public key* bit encryption scheme that is not circular secure and (2) we show a separation for unbounded length cycles.

Our second family of negative results relate to a grouping of constructions related to the well known Fujisaki-Okamoto transformation that achieves chosen ciphertext security in the random oracle model from any scheme which is IND-CPA secure. Included in this grouping are: the Bellare et al. [67] transformation from an IND-CPA scheme to an injective trapdoor function, two transformations from IND-CPA to IND-CCA security due to Fujisaki and Okamoto [68], [69] and the deterministic encryption construction of Bellare, Boldyreva and O'Neill [70].

All of the constructions follow a similar paradigm where they encrypt a string x under random coins determined from H(x). (How the string x is construed varies somewhat between the schemes.) The works above show that if H is presented as an oracle access to a random function, the transformation results in a secure scheme under the relevant

definition.

We give a random oracle uninstantiability [71] result where using lockable obfuscation there exists an encryption scheme where for any hash function of up to apriori bounded size the applying the above transformations will result in an insecure encryption scheme — the message will be easily discovereable. If we add the assumption of fully homomorphic encryption we can remove the bounded size restriction.

Brzuska, Farshim and Mittelbach [72] achieved these results using indistinguishability obfuscation.⁴ We realize our results by simply translating the BFM result to move from indistinguishability obfuscation to lockable obfuscation. Again, this is possible because the programs obfuscated in the BFM paper follow the same lockable friendly form.

Indistinguishability Obfuscation for Rejecting Programs: For our final application we now consider a new notion of obfucation that we call indistinguishability obfuscation for rejecting programs and show how to construct it from lockable obfuscation and witness encryption [73] for circuit satisfiability.

Obfuscators that meet this notion will be defined over boolean circuits. Like indistinguishability obfuscation our obfuscator will take in any (not necessarily rejecting) boolean circuit C in a class and output an obfuscated program that is functionally equivalent to C. However, the security guarantees given by such an obfuscator are limited to "rejecting" programs. Informally, they state that no PPT adversary can distinguish between circuits C_0 and C_1 so long as for all inputs x $C_0(x) = C_1(x) = 0$. In contrast, standard indistinguishability obfuscation security allows C_0, C_1 to have arbitrary (both 0 and 1) outputs so long as they are functionally equivalent.

Our construction is simple and follows along the same conceptual lines as our techniques for building predicate encryption with one sided security from Attribute-Based Encryption.

Concurrent Work: In an independent and concurrent work, Wichs-Zirdelis proposed a similar notion called Obfuscation for Compute-and-Compare Programs. While the notions are very similar, the syntax is different. A compute-and-compare program CC[f,y] is defined by a function f and a value g. Obfuscation of CC[f,y] outputs a program P such that P, on any input g, outputs 1 if g (g). For security, [74] require that for a randomly chosen g (from a high-entropy distribution), the obfuscation of g (g) is indistinguishable from simulated obfuscation, where the simulator gets only size of g and g. Wichs-Zirdelis also extend this notion where the obfuscation algorithm also takes as input a message g together with g, g, and the obfuscated

program P on input x, outputs m if f(x) = y. Security requires that the message m is also hidden. This message-based version of obfuscation of compute-and-compare programs is identical to our notion of lockable obfuscation, modulo the distribution of lock y, which is uniform in our case and can be any high-entropy distribution in their case. However, this gap can be simply bridged by using an PRG for pseudo-entropy seeds instead of a regular PRG.

Both works have a few applications in common. This includes the ABE to predicate encryption transformation, witness encryption to reject-iO and circular security counterexamples. In addition, we show new uninstantiability results in the random oracle model. Applications unique to [74] are transformation from any secure sketch [75] to private secure sketch [76], and obfuscation for conjunctions/affine testers.

A. Overview of our Lockable Obfuscation Construction

We will now describe our lockable obfuscation scheme for a family of poly-depth circuits. The construction is described in detail in the full version. At a high level, our scheme can be divided into three components — (1) A lockable obfuscation scheme for a family of low-depth circuits and 1-bit messages, (2) a bootstrapping mechanism to amplify to lockable obfuscation for a family of poly-depth circuits and 1-bit messages, and (3) extending to lockable obfuscation for a family of poly-depth circuits and multi-bit messages. (Note in our actual construction, we combine the first two components into one for technical reasons.)

Lockable Obfuscation for Low-Depth Circuits and 1-Bit Messages.: The primary ingredients of our construction are low-depth pseudorandom generators (PRGs), lattice trapdoors [77], telescoping products/cascading cancellations [64], [63], [12] and oblivious sequence transformation [66].

First, let us recall the notion of permutation branching programs, lattice trapdoors and oblivious sequence transformation. A permutation branching program of length L and width w can be represented using w states, 2L permutations $\sigma_{j,b}$ over states for each level $j \leq L$, an input-selector function $\operatorname{inp}(\cdot)$ which determines the input read at each level, and an accepting and rejecting state. The program execution starts at state 1 of level 0. Suppose the branching program reads first input bit (say b) at level 1 (i.e., $\operatorname{inp}(1) = 1$). Then, the state of the program changes to $\sigma_{1,b}(\operatorname{st})$. Such a process can be carried out (iteratively) to compute the final program state at level L. Depending upon the final state, the program either accepts or rejects.

A lattice trapdoor generation algorithm can be used to sample a (uniformly looking) matrix $\bf A$ together with a trapdoor $T_{\bf A}$. The trapdoor can be used to compute, for any

⁴Technically, their result with no bounds on the hash function required indistinguishability obfuscation for Turing Machines with unbounded input. However, this could have been replaced with indistinguishability obfuscation for circuits and fully homomorphic encryption.

matrix \mathbf{U} , a low norm matrix \mathbf{S} such that $\mathbf{A} \cdot \mathbf{S} = \mathbf{U}.^5$ As a result, the matrix \mathbf{S} can be used to 'transform' any matrix $\widetilde{\mathbf{A}} \approx \mathbf{A}$ to another matrix $\widetilde{\mathbf{U}} \approx \mathbf{U}$. Oblivious sequence transformation is a technique that enables sampling a sequence of matrices $\mathbf{B}_1, \ldots, \mathbf{B}_w$ along with some trapdoor such that for any sequence of matrices $\mathbf{U}_1, \ldots, \mathbf{U}_w$, one can construct a short matrix \mathbf{X} such that $\mathbf{B}_i \cdot \mathbf{X} = \mathbf{U}_i$ for all i (and \mathbf{X} is oblivious of i).

Moving on to our construction, at a high level the obfuscator starts by first generating a sequence of permutation branching programs corresponding to the circuit C (where each branching program computes one output bit), and then encoding the state transition permutations for each level for every branching program using the technique of oblivious sequence transformation. Let ℓ be the output length of C. In other words, for obfuscating circuit C under lock α with message msg, the obfuscator first expresses C as a set of width 5 permutation branching programs $\{BP^{(i)}\}_i$ of polynomial length L, where for each $i \in [\ell]$ BP⁽ⁱ⁾ computes the i^{th} output bit of circuit $C.^6$ Without loss of generality, we can assume that all branching programs have a common input selector function $inp(\cdot)$ such that inp(j) bit of the input is read at level j. The obfuscation algorithm continues by choosing $5(L+1)\ell$ matrices, one matrix for each (level, state) of each branching program.⁷ Let $\mathbf{B}_{j,w}^{(i)}$ be the matrix corresponding to state $w \in [5]$ at level $j \leq L$ for branching program $\mathsf{BP}^{(i)}$, $i \in [\ell]$. For every $i \in [\ell]$, the matrices $\{\mathbf{B}_{j,1}^{(i)}, \ldots, \mathbf{B}_{j,5}^{(i)}\}$ for the first L levels (i.e., all but top-level matrices) are sampled such that they have a common trapdoor $T_j^{(i)}$, i.e. using oblivious sequence transformation. The top level matrices because sequence transformation. The top-level matrices, however, are sampled uniformly at random without a trapdoor subject to the constraint that the top-level matrices corresponding to lock string α sum to a special fixed matrix depending upon the message msg. More formally, for each $i \in [\ell]$, let q be the LWE modulus, and $acc^{(i)}$ and $rej^{(i)}$ be the accepting and rejecting states for $\mathsf{BP}^{(i)},$ then the obfuscator chooses the matrices $\mathbf{B}_{L,\mathsf{acc}^{(i)}}^{(i)}, \mathbf{B}_{L,\mathsf{rej}^{(i)}}^{(i)}$ such that

$$\begin{split} \sum_{i \ : \ \alpha_i = 0} \mathbf{B}_{L, \mathsf{rej}^{(i)}}^{(i)} + \sum_{i \ : \ \alpha_i = 1} \mathbf{B}_{L, \mathsf{acc}^{(i)}}^{(i)} = \\ \begin{cases} \mathbf{0}^{n \times m} & \text{if msg} = 0. \\ \sqrt{q} \cdot \left[\mathbf{I}_n \, || \, \mathbf{0}^{n \times (m-n)} \right] & \text{if msg} = 1. \end{cases} \end{split}$$

For each level $j \in [L]$, the obfuscation algorithm also chooses two low-norm matrices $\mathbf{S}_{j}^{(0)}$ and $\mathbf{S}_{j}^{(1)}$ (these are shared across all branching programs), and computes 2ℓ

low-norm matrices $\{\mathbf{C}_j^{(i,0)}, \mathbf{C}_j^{(i,1)}\}_{i,j}$ such that for every state $w \in [5]$, $\mathbf{B}_{j-1,w}^{(i)} \cdot \mathbf{C}_j^{(i,0)} \approx \mathbf{S}_j^{(0)} \cdot \mathbf{B}_{j,\sigma_{j,0}^{(i)}(w)}^{(i)}$ and $\mathbf{B}_{j-1,w}^{(i)} \cdot \mathbf{C}_j^{(i,1)} \approx \mathbf{S}_j^{(1)} \cdot \mathbf{B}_{j,\sigma_{j,1}^{(i)}(w)}^{(i)}$. That is, the matrices $\mathbf{C}_j^{(i,0)}$ and $\mathbf{C}_j^{(i,1)}$ represent the state transition from level j-1 to j when bit 0 or 1 is read at step j of branching program execution. For each $i \in [\ell], j \in [L]$, the $\mathbf{C}_j^{(i,\ell)}$ matrices can be generated using the lattice trapdoors $T_j^{(i)}$. The obfuscation algorithm outputs these matrices $\{\mathbf{C}_j^{(i,0)}, \mathbf{C}_j^{(i,1)}\}_{i,j}$ together with the base-level matrices $\{\mathbf{B}_{0,1}^{(i)}\}_i$ as the final obfuscated program.

At a high level, one could visualize the obfuscated program which consists of the base-level matrices $\{{\bf B}_{0,1}^{(i)}\}_i$ and matrices $\{\mathbf{C}_{j}^{(i,0)}, \mathbf{C}_{j}^{(i,1)}\}_{i,j}$ as "encodings" of the branching program starting states and state transition permutations, respectively. Therefore, evaluating an obfuscated program on some input x will be analogous to evaluating the branching programs $BP^{(i)}$ on input xdirectly. Fix some $i \in [\ell]$. Suppose the first input bit x_1 is read at level 1. Then evaluation of $BP^{(i)}$ at level 1 would map the state 1 at level 0 to state $\sigma_{1,x_1}^{(i)}$ at level 1. Analogously, the obfuscation evaluator can compute $\mathbf{B}_{0,1}^{(i)}\cdot\mathbf{C}_{1}^{(i,x_{1})}\approx\mathbf{S}_{1}^{(x_{1})}\cdot\mathbf{B}_{1,\sigma_{1,x_{1}}^{(i)}}^{(i)}$. In general, if the program state at level j-1 during execution is w, then the evaluator will accumulate the product of the form $\Gamma_{j-1} \cdot \mathbf{B}_{j,w}^{(i)}$, where Γ_{j-1} is a product of j-1 low-norm matrices. This can be easily verified as follows. Suppose the next bit read is b, then the new state at level j will be $\sigma_{j,b}^{(i)}(w)$, thus the new accumulated product during obfuscation evaluation will be $\Gamma_{j-1} \cdot \mathbf{B}_{j,w}^{(i)} \cdot \mathbf{C}_{j}^{(i,b)} \approx \Gamma_{j} \cdot \mathbf{B}_{j+1,\sigma_{j,b}^{(i)}(w)}^{(i)}$, where $\Gamma_j = \Gamma_{j-1} \cdot \mathbf{S}_j^{(b)}$. Therefore, the invariant is maintained. Note that the matrix Γ_L will be same for all branching programs since the low-norm matrices $\mathbf{S}_j^{(0)}$ and $\mathbf{S}_j^{(1)}$ are shared across all branching programs.

Continuing this way, the evaluator can iteratively compute the matrix product at the top. Thus, for each branching program, the accumulated product at the top will either be $\approx \Gamma_L \cdot \mathbf{B}_{L,\mathrm{acc}^{(i)}}^{(i)}$ or $\approx \Gamma_L \cdot \mathbf{B}_{L,\mathrm{rej}^{(i)}}^{(i)}$, depending on whether $C(x)_i = 0$ or 1. Let $\mathbf{\Delta}^{(i)} = \Gamma_L \cdot \mathbf{B}_{L,\mathrm{st}^{(i)}}^{(i)}$, where $\mathrm{st}^{(i)} = \mathrm{acc}^{(i)}$ or $\mathrm{rej}^{(i)}$ depending on $C(x)_i$. Finally, the evaluator simply sums the top-level accumulated products $(\approx \mathbf{\Delta}^{(i)})$ and checks whether the norm of the final summed matrix lies in appropriate range. More concretely, consider the case when $C(x) = \alpha$ and $\mathrm{msg} = 0$, then $\sum_i \mathbf{\Delta}^{(i)} = \Gamma_L \cdot \sum_i \mathbf{B}_{L,\mathrm{st}^{(i)}}^{(i)} = \mathbf{0}^{n \times m}$. Since the final top-level matrix sum is close to $\sum_i \mathbf{\Delta}^{(i)}$, thus it will have norm close to 0, and hence the evaluator can simply test this and output 0 as the message.

 $^{^5}$ For ease of exposition, we will use the notation ${\bf A}^{-1}(\cdot)$ to represent the pre-image operation. In the formal description of our algorithms later, we use the pre-image sampling algorithm SamplePre.

 $^{^6}$ From Barrington's Theorem [78], we know that for every ${\bf NC}^1$ circuits there exists a width 5 permutation branching program of polynomial length.

⁷Note that if a branching program has length L, then it has L+1 levels.

Similarly we could argue correctness for the cases when msg = 1 or $C(x) \neq \alpha$. However, our current proof techniques do not seem sufficient for proving the security of above construction. The reason is that there is an inherent tension in setting scheme parameters while basing security on LWE. This is discussed in detail later in the full version. We were able to bypass this problem by obfuscating an "expanded" circuit, which evaluates a low-depth pairwise independent hash function h and a low-depth pseudorandom generator PRG with a large enough polynomial stretch (in succession) on the output of circuit C, instead of directly obfuscating circuit C using the above matrix encoding procedure.

In other words, let Q be the circuit that on input x, outputs $\operatorname{PRG}(h(C(x)))$. Observe that if h and PRG can be computed by low-depth circuits (NC^1), then Q also can be computed by an NC^1 circuit (since C is assumed to be a log-depth circuit). Therefore, Q can be expressed by a set of width 5 permutation branching programs of polynomial length L as well. Additionally, now the matrix component generation procedure will use $\beta = \operatorname{PRG}(h(\alpha))$ as the lock instead of α . This modification is sufficient to avoid the tension between scheme parameters, and also allows us to prove security of our scheme under LWE with only polynomial hardness. This completes the description of our lockable obfuscation scheme for low-depth circuits and 1-bit messages.

Bootstrapping Lockable Obfuscation.: Let $\mathcal{O}_{\mathsf{NC}^1}$ be a lockable obfuscator for log-depth circuits. We will use leveled homomorphic encryption (LHE) with an \mathbf{NC}^1 decryption circuit to bootstrap $\mathcal{O}_{\mathsf{NC}^1}$ to an obfuscator that works for any depth d. The obfuscator gets as input a circuit C of depth d, a string α and a message msg. It first chooses the LHE secret-evaluation keys and encrypts the circuit C. Let Q be the circuit which takes as input an LHE ciphertext and decrypts it using the hardwired LHE secret key and outputs the decrypted string. Note that the circuit Q is a logarithmic depth circuit. The obfuscator outputs $\mathcal{O} \leftarrow \mathcal{O}_{\mathsf{NC}^1}(Q,\alpha,\mathsf{msg})$ together with the encryption of C and the LHE evaluation key.

Evaluating on input x. Let $U_x(\cdot)$ be the universal circuit with input x hardwired (that is, it takes a circuit C as input and outputs C(x)). The evaluation algorithm first homomorphically evaluates the circuit U_x on the encryption of C. This results in an LHE ciphertext ct, which is an encryption of C(x). It then evaluates the obfuscation $\mathcal O$ on input ct, and outputs the resulting string. Using the correctness of $\mathcal O$ and the LHE scheme, we can argue that if $C(x) = \alpha$, then the evaluation outputs msg, and it outputs \bot otherwise.

The security proof here is fairly simple. Using the security of the underlying obfuscator \mathcal{O}_{NC^1} , we first switch the obfuscation \mathcal{O} to be a simulated obfuscation. Once the obfuscator \mathcal{O} is simulated, the obfuscator no longer needs

the LHE secret key. Therefore, we can now replace the LHE encryption of C with encryption of zeros, thereby erasing all the information about circuit C except its size. Therefore, the final simulator simply outputs an encryption of zeros, together with a simulated obfuscation, and this is indistinguishable from the honestly computed obfuscation.

Extending Lockable Obfuscation for 1-Bit Messages to Multi-Bit Messages.: We would like point out that the standard repitition method for extending message space does not work for lockable obfuscation schemes because the security is only guaranteed when the adversary does not know the lock string α . However, we observe there are still multiple ways to extend its message space. We briefly discuss two possible extensions. One option could be to encode a multi-bit message directly in the top-level matrices. Currently, the top-level matrices are set to sum to $\mathbf{0}^{n\times m}$ if $\operatorname{msg} = 0$, otherwise to $\sqrt{q} \cdot \left[\mathbf{I}_n \mid\mid \mathbf{0}^{n\times (m-n)}\right]$. However, if we interpret the mesage msg as an integer $v < \sqrt{q}/2$, then we could simply set the sum to be $v \cdot \sqrt{q} \cdot \left[\mathbf{I}_n \mid\mid \mathbf{0}^{n\times (m-n)}\right]$.

The second extension could be carried more generally without exploiting the mathematical structure of the underlying obfuscation scheme. The high level idea is to again "expand" the circuit C using a pairwise independent hash function and a pseudorandom generator before obfuscation. Suppose the lock α be a string of length k. Let $\beta = \operatorname{PRG}(h(\alpha))$ and $|\beta| = \ell \cdot k$. To obfuscate circuit C under lock α for an ℓ -bit message msg, the multi-bit obfuscator (for each $i < \ell$) independently obfuscates the circuit Q[i] under lock $\beta[i]$ for message msg, using the 1-bit obfuscation scheme, where circuit Q[i] denotes the circuit that outputs the $i \cdot k + 1, \ldots, (i+1) \cdot k$ output bits of circuit $\operatorname{PRG}(h(C(\cdot)))$ and $\beta[i] = \beta_{i \cdot k+1}, \ldots, \beta_{(i+1) \cdot k}$. The security proof follows from a simple hybrid argument. This transformation is described later in the full version.

This completes the technical overview of our lockable obfuscation scheme.

B. More on Related Encoding Works

The idea of using lattice trapdoors for constructing multilinear maps was first seen in the work of Gentry, Gorbunov and Halevi (GGH) where they proposed a candidate for graph-induced multilinear maps [12]. Their work builds upon the homomorphic encryption scheme of Gentry, Sahai and Waters [79] which could be considered as the starting point of cascading cancellations technique. In [12], there is a fixed (directed acyclic) graph G=(V,E), and plaintext messages are associated with edges. Each vertex u has an associated matrix \mathbf{A}_u and a secret parameter T_u which is the trapdoor for \mathbf{A}_u . The plaintext space consists of matrices, and the encoding of a matrix \mathbf{M} along an edge (u,v) is $\mathbf{A}_u^{-1}(\mathbf{M}\cdot\mathbf{A}_v+\text{noise})$. Note that given an encoding of matrix \mathbf{M}_1 for edge (u,v) and an encoding of matrix

 \mathbf{M}_2 for edge (v,w), one can compute an encoding of $\mathbf{M}_1 \cdot \mathbf{M}_2$ along path (u,v,w). Informally, their intuition was that given all the encodings it should be easy to compute an encoding for any path in graph G, however it should remain hard for anyone to generate encodings over any non-path combination of vertices. Using these multilinear maps, GGH gave candidate constructions for obfuscation and multipartite key-agreement. However, there were no security proofs for these candidates, and their key-agreement protocol was later shown to be broken [18]. In a later work, Brakerski et al. [80] gave a construction for obfuscating conjunctions based on an entropic variant of Ring-LWE. In that work, they observed that if the underlying graph was a straight-line graph (i.e., an ordered sequence of vertices), then the corresponding GGH multilinear maps provide some provable security properties.

In a more recent work, Koppula and Waters [64] used what they called cascading cancellations technique for constructing k-circular security separations. Their construction deviates from the GGH/BVWW paradigm in two crucial aspects. First, their construction involves multiple strands (say ℓ) of length k, instead of just a single strand. Unlike the previous works, which involved directly comparing two distinct encodings (or, components) along the same path for equality, they first combined (using matrix multiplications) all the k components for each different strand, and then summed the ℓ final components (one for each strand) to perform an equality check with a fixed value. Second, what is mechanically close to the "plaintext" encoded values in GGH is viewed here as simply random matrices (and just part of the overall randomness) and not a value to be encoded. This differs from GGH in which the the elements being encoded were labeled as "plaintext" values. For instance, the elements that were encoded in the GGH obfuscation candidate were the state transition matrices of the actual branching program beign obfuscated and thus can reflect some stronger semantics. Concurrently, Alamati and Peikert [63] also provided circular security separations, that had a cancellation type effect although with a different technical approach.

Most recently, Goyal, Koppula and Waters [66] further advanced the existing techniques for constructing bit-circular security separations. One of their most important contributions was an alternative mechanism to encode and *hide* (branching) programs using lattice trapdoors. To this end, they introduced a novel technique which they call oblivious sequence transformation. This is a significant departure from prior works as previously most works generated the matrix components (or encodings) independently, however it was essential in their construction that the components be jointly generated. At a high level, they provided new techniques to encode and hide a permutation between a sequence of nodes. Informally, they gave a mechanism to encode a permutation between nodes u_1, \ldots, u_5 and w_1, \ldots, w_5 such that given a node u_i one could obliviously go to its corresponding

node w_i . Another important aspect of their work was to encode a log-depth Pseudo Random Generator (PRG) using the oblivious sequence transformation technique such that the PRG could be publicly evaluated and if the output of computation is some fixed (but unknown) value, then it could be efficiently tested. This seems to be one of the most important technical aspect of their work since in order to prove security from the LWE assumption as well as guarantee efficient testing, encoding a log-depth PRG with a large polynomial stretch is essential. We finally remark that Canetti and Chen [81] recently used the LWE assumption to achieve 1-collusion secure constrained PRFs for NC1 with constraint hiding. Their construction involves a single strand like BVWW and their embedding of a branching program follows the GGH style of putting branching programs into the encoded plaintext values much more closely.

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